

SC1533-01FTG Series Bidirectional TVS Diode Arrays

3.3V, 15A, in SOD323 package for Protecting General IO

Problem/Solution

SC1533-01FTG bidirectional TVS diode arrays, fabricated in a proprietary silicon avalanche technology, protect electronic equipment against high electrostatic discharge (ESD) levels. These TVS diode arrays safely absorb repetitive ESD strikes of ± 30 kV (contact and air discharge as defined in IEC 61000-4-2) without any performance degradation. They also safely dissipate 15 A 8/20 μ s surge events (see IEC 61000-4-5, 2nd Edition).

Technical resources *(Click on below icons to learn more)*

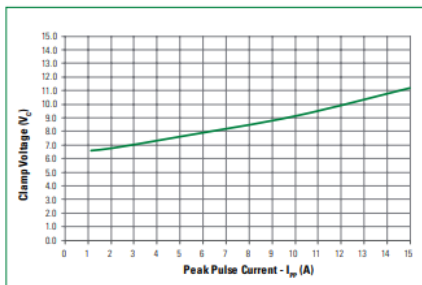


Series Page

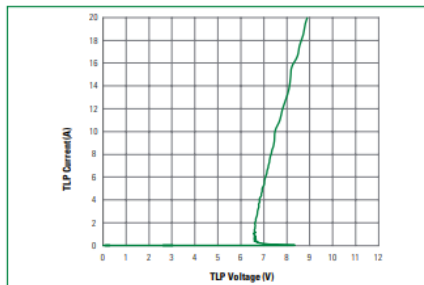


Datasheet

Clamping Voltage vs I_{PP}



Positive Transmission Line Pulsing (TLP) Plot



Benefits

- Safely dissipates high surge events
- Compliant with numerous IEC standards for ESD suppression and lightning surge protection
- Moisture Sensitivity Level (MSL-1)

Features

- ESD, IEC 61000-4-2, ± 30 kV contact/air
- EFT, IEC 61000-4-4, 40 A (5/50 ns)
- Maximum surge tolerance, IEC 61000-4-5, 2nd Edition, 15 A (8/20 μ s)



Markets/Applications

- Appliance
- Battery protection
- Computer peripherals
- Medical equipment
- Notebooks/desktops/servers
- Point-of-Sale terminals
- Switches/buttons
- Test equipment/instrumentation



SC1533-01LTG Series Bidirectional TVS Diode Arrays

3.3V, 15A, in SOD523 package for Protecting General IO

Problem/Solution

SC1533-01LTG bidirectional TVS diode arrays, fabricated in a proprietary silicon avalanche technology, protect electronic equipment against high electrostatic discharge (ESD) levels. These TVS diode arrays safely absorb repetitive ESD strikes of ± 30 kV (contact and air discharge as defined in IEC 61000-4-2) without any performance degradation. They also safely dissipate a 15 A 8/20 μ s surge event (see IEC 61000-4-5, 2nd Edition).

Technical resources *(Click on below icons to learn more)*

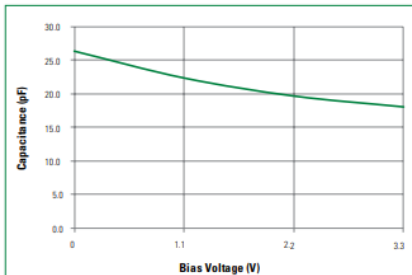


Series Page

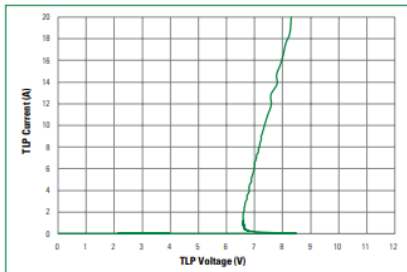


Datasheet

Capacitance vs Reverse Bias



Positive Transmission Line Pulsing (TLP) Plot



Benefits

- Safely dissipates high surge events
- Compliant with numerous IEC standards for ESD suppression and lightning surge protection
- Moisture Sensitivity Level (MSL-1)



Features

- ESD, IEC 61000-4-2, ± 30 kV contact/air
- EFT, IEC 61000-4-4, 40 A (5/50 ns)
- Maximum surge tolerance, IEC 61000-4-5, 2nd Edition, 15 A (8/20 μ s)

Markets/Applications

- Appliance
- Battery protection
- Computer peripherals
- Medical equipment
- Notebooks/desktops/servers
- Point-of-Sale terminals
- Switches/buttons
- Test Equipment/instrumentation



Expertise Applied | Answers Delivered